

Computer-Aided Design of MESFET Distributed Amplifier

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An automatic CAD procedure for the design of a MESFET distributed amplifier is described. This procedure is based on a novel design method called design-by-simulation. A combinatorial optimization process called simulated annealing is applied in this design procedure to provide information about the number of stages, the MESFET model parameters, and the characteristics of microstrips used for matching networks in an amplifier which matches the desired frequency response. This procedure is fully automatic and the only input needed is the desired gain and the 1-dB point.

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